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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Allen et al.
Docket No: YOR920030175US1
Serial No.: 10/661,041
Filing Date: September 12, 2003
Group: 2811
Examiner: Cuong Q. Nguyen

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: Robert H. Blaha Date: September 17, 2004

Title: Techniques for Patterning Features in Semiconductor Devices

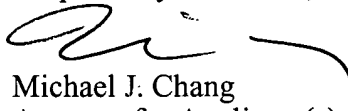
STATUS REQUEST

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

To date, we have received no communication or official action from the Examiner in charge of the above-identified patent application. Please let us know the status of this case.

Respectfully submitted,



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